In the Specification

Please amend the title on pages 1 and 28 of the Specification as follows:

ACQUIRING ~~A FREQUENCY~~ FREQUENCY AND PHASE OFFSET ESTIMATES USING FREQUENCY DOMAIN ANALYSIS

Please amend page 1, lines 7-10, of the Specification as follows:

This invention relates generally to the field of signal processing and more specifically to acquiring ~~a frequency~~ frequency and phase offset estimates using frequency domain analysis.

Please amend page 7, lines 17-30, of the Specification as follows:

Automatic gain ~~controller 32~~ controller 30 estimates the channel gain of the received signal. Automatic gain controller 30 may comprise one or more detectors operable to maintain a constant amplitude of a signal by reducing or increasing the gain of the signal in accordance with the strength of the signal. Delay 32 delays the received signal for a suitable amount of time such that the signal and the channel gain estimate for the signal arrive at multiplier 34 at substantially the same time. Delay 32 may comprise a delay circuit, such as a first-in-first-out (FIFO) circuit that introduces a time delay. Multiplier 34 removes the channel gain from the received signal. Decimator 36 reduces the rate of the received signal.